

Sheet <u>1</u> of <u>1</u>		FORM PTO 1449 (modified)		ATTY DOCKET NO. 239/176		SERIAL NO. Unassigned <u>10/660,802</u>		
U.S. DEPARTMENT OF COMMERCE				APPLICANT Hyung-rok OH et al.				
				FILING DATE Concurrently		GROUP Unassigned <u>2818</u>		
U.S. PATENT DOCUMENTS								
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)								
<u>2</u> 		2002 Symposium on VLSI Circuits Digest of Technical Papers, DURLAM et al. - A low power 1Mbit MRAM, etc..						
		pages 158-161.						
EXAMINER		<u>Lee Ahnwoong</u>			DATE CONSIDERED			<u>9/04</u>